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APPLICATION NO.		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/762,451		01/23/2004	Hidenori Hasegawa	2004-0096A	5478
513	7590	07/26/2005		EXAMINER	
	•	ND & PONACK, L	TOLEDO, FERNANDO L		
	2033 K STREET N. W. SUITE 800 WASHINGTON, DC 20006-1021				PAPER NUMBER
WASHING					2823
				DATE MAILED: 07/26/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
·	10/762,451	HASEGAWA, HIDENORI					
Office Action Summary	Examiner	Art Unit					
	Fernando L. Toledo	2823					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REF THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a r - If NO period for reply is specified above, the maximum statutory perion - Failure to reply within the set or extended period for reply will, by state that the period for reply will, by state that the period for reply will, by state that the period for reply will and the period for reply will. The period for reply will, by state that the period for reply will be stated that the period for reply wi	N. 1.136(a). In no event, however, may a reply be timely within the statutory minimum of thirty (30) days od will apply and will expire SIX (6) MONTHS from tute, cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).					
Status							
1) Responsive to communication(s) filed on 23	January 2004.						
2a)☐ This action is FINAL . 2b)☒ Ti	his action is non-final.						
·	Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4) ☐ Claim(s) 1-7 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-7 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or election requirement.							
Application Papers							
9)⊠ The specification is objected to by the Examiner.							
10)⊠ The drawing(s) filed on <u>24 January 2004</u> is/are: a)⊠ accepted or b)⊡ objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the							
Priority under 35 U.S.C. § 119							
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
Attachment(s)		,					
1) Notice of References Cited (PTO-892)	4) Interview Summary	(PTO-413)					
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/C Paper No(s)/Mail Date <u>20040124</u>. 	Paper No(s)/Mail Da						

DETAILED ACTION

Specification

1. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- Claims 1 7 are rejected under 35 U.S.C. 102(e) as being anticipated by Minamio et al.
 (US Patent Application Publication US 2003/0015775 A1).
- 4. In re claims 1, 3 and 5, Minamio, in the US Patent Application Publication US 2003/0015775 A1; figures 1A 10D discloses arranging several of linear leadframes (1 and 4) side by side separately from each other (Figure 1B); mounting several semiconductor chips 3 having a first main surface with several of electrode pads on a second main surface facing the main surface, each of the semiconductor chips placed over the several of the linear leadframes and separated from each other in a direction extending the linear leadframes with the second main surface of the semiconductor chip thereon (Figure 6B); joining several of the electrode pads to the several of the linear leadframes with bonding wires 5 (Figure 6C); forming the

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encapsulation part 6 for encapsulating the semiconductor chip and the bonding wire and an interframe encapsulation part for burying a space between the adjacent linear leadframes exposed outside the encapsulation part (Figure 6D); forming a groove part for cutting all the linear leadframes places under the second main surface in a vertical direction to the direction extending the linear leadframes (Figures 6A - 7); and cutting the leadframes and the interframe encapsulation parts exposed between the several of the semiconductor chips to separate into a semiconductor device having the semiconductor chip, a first external terminal row and second external terminal row facing each other as sandwich the groove part (Figure 7).

- 5. In re claims 2 and 4 Minamio discloses wherein the mounting is conducted by exposing outermost leadframes on both sides among the several of the linear leadframes arranged side by side, and the joining does not join the bonding wires to the outermost leadframes (Figure 6B).
- 6. In re claim 6, Minamio discloses wherein the encapsulation part and the frame encapsulation part are formed in one piece (Figure 6D).
- 7. In re claim 7, Minamio discloses wherein the several of the first external terminals are separated in stripes (Figure 1B).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fernando L. Toledo whose telephone number is 571-272-1867. The examiner can normally be reached on Mon-Thu 7am to 5:30pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Smith can be reached on 571-272-1907. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Fernando L. Toledo

Examiner

Art Unit 2823

flt

24 July 2005